



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hakewill, et al.
App. No. : 09/418,663
Filed : October 14, 1999
For : **METHOD AND APPARATUS
FOR MANAGING THE
CONFIGURATION AND
FUNCTIONALITY OF A
SEMICONDUCTOR DESIGN**
Examiner : Garcia-Otero, E.
Group Art Unit: 2123




27299

PATENT, TRADEMARK OFFICE

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on

Tuesday, February 25, 2003
(Date)


Robert F. Gazdzinski, Reg. No. 39,990

AMENDMENT AND RESPONSE TO OFFICE ACTION

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Technology Center 2100

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In response to the Office Action dated August 26, 2002 ("Office Action"), please amend the above-identified application as follows:

IN THE SPECIFICATION

1. On Page 1 (as previously amended), lines 18-19, please amend the text as follows:

20 (X) --The invention relates generally to the field of [semiconductor] integrated circuit design [and layout and computer automated design (CAD) for semiconductors].--

2. On Page 13, line 14, please delete the trademark ("TM") symbol immediately following the word "Wizard".